

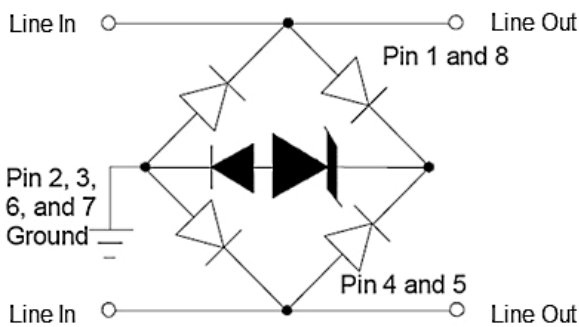
Description

The LC03-3.3 transient voltage suppressor is designed to protect components which are connected to high speed data and telecommunication lines from voltage surges caused by lightning, electrostatic discharge(ESD), and electrical fast transients(EFT). TVS Diodes are ideal for use as board level protection of sensitive semiconductor components. The LC03-3.3 combines a TVS diode with a rectifier bridge to provide transient protection in both common and differential mode with a single device. The LC03-3.3 utilizes EPD technology for superior electrical characteristics at 3.3 v. The capacitance of the device is minimized to ensure correct signal transmission on high speed lines. The LC03-3.3 meets the short-haul(intra-building) transient immunity requirements of Bellcore 1089 for telecommunications applications.

Mechanical Characteristics

- ◆ JEDEC SO-8 package
- ◆ ROHS/WEEE Compliant
- ◆ Molding compound flammability rating: UL 94V-0
- ◆ Marking: Part number, date code
- ◆ Packaging: Tube or Tape and Reel per EIA 481

Dimensions and Pin Configuration



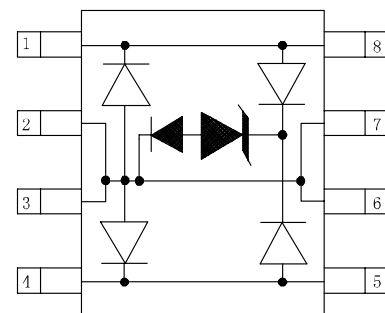
Circuit and Pin Schematic

Features

- ◆ 2000W peak pulse power(8/20 μ s)
- ◆ Transient protection for high-speed data lines to Bellcore 1089(Intra-Building)100A(2/10 μ s)
ITU K.20 I_{PP}=40A(5/310 μ s)
IEC 61000-4-2(ESD) \pm 15KV(air), \pm 8KV(contact)
IEC 61000-4-4(EFT) 40A(5/50ns)
IEC 61000-4-5(Lightning)100A(8/20 μ s)
- ◆ Protects two lines in common and differential mode
- ◆ Low capacitance for high-speed interfaces
- ◆ Low operating voltage(3.3V)
- ◆ Low clamping voltage
- ◆ Integrated structure saves board space and Increases reliability
- ◆ Solid-state EPD technology

Applications

- ◆ Base Station
- ◆ Analog Inputs
- ◆ Switch Systems
- ◆ 10/100/1000 Ethernet
- ◆ WAN/LAN Equipment



SO-8 Outline

Ordering Information

Part Number	Marking	Packaging	Reel Size
LC03-3.3	SC YYWW LC03-3.3	2500/Tape & Reel	13inch

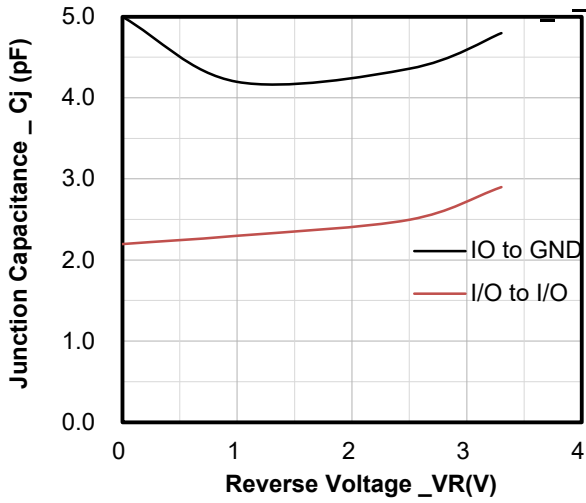
Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak Pulse Power(8/20 μs)	Ppk	2000	W
Peak Pulse Current(8/20 μs)	I _{PP}	100	A
Lead Soldering Temperature	T _L	260(10 sec.)	$^{\circ}\text{C}$
Operating Temperature Range	T _J	-55 to +125	$^{\circ}\text{C}$
Storage Temperature Range	T _{stg}	-55 to +150	$^{\circ}\text{C}$

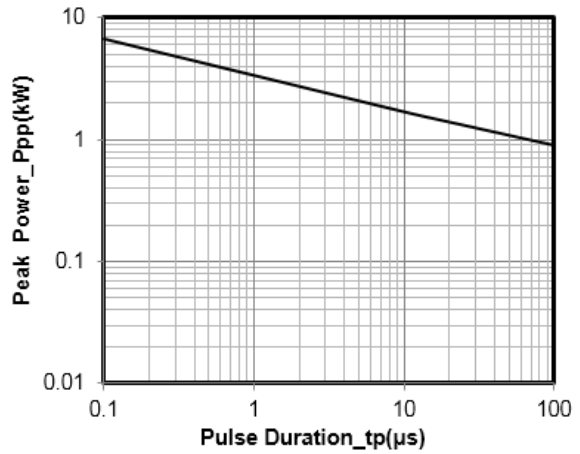
Electrical Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Working Voltage	V _{RWM}			3.3	V	
Punch-Through Voltage	V _{PT}		4.7		V	I _T = 2 μA
Breakdown Voltage	V _{BR}		5.0		V	I _T = 1mA
Snap-Back Voltage	V _{SB}		5.4		V	I _T = 50mA
Reverse Leakage Current	I _R			0.5	μA	V _{RWM} = 3.3V
Clamping Voltage	V _C			6	V	I _{PP} = 1A (8 x 20 μs pulse), any I/O pin to ground
Clamping Voltage	V _C			15	V	I _{PP} = 100A (8 x 20 μs pulse), I/O pin to ground any
Junction Capacitance	C _J		5.0	8.0	pF	V _R = 0V, f = 1MHz, between I/O pins and ground
Junction Capacitance	C _J		2.5	4.0	pF	V _R = 0V, f = 1MHz, between I/O pins

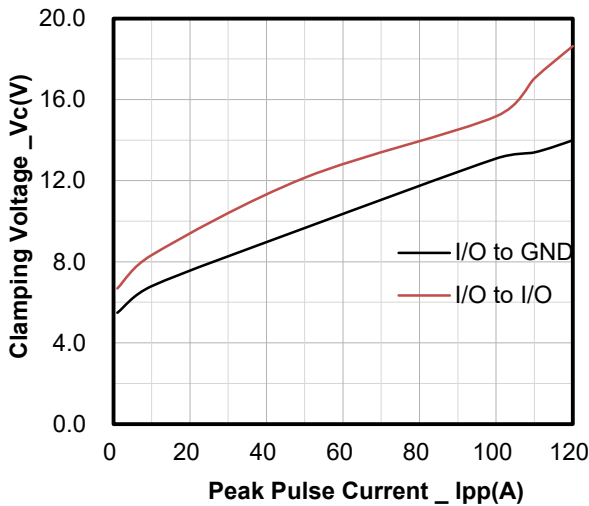
Typical Performance Characteristics ($T_A=25^\circ\text{C}$ unless otherwise Specified)



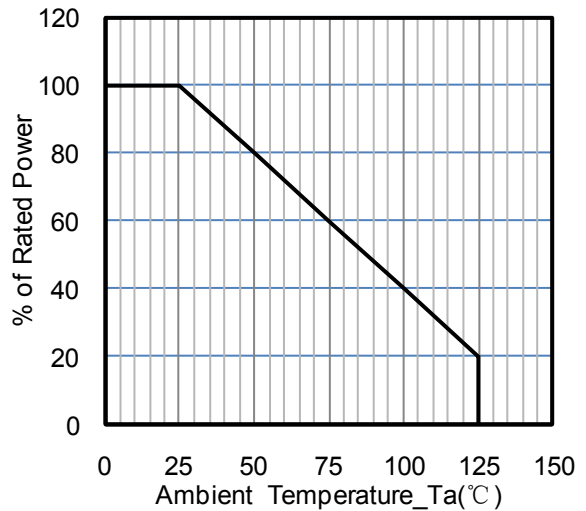
Junction Capacitance vs. Reverse Voltage



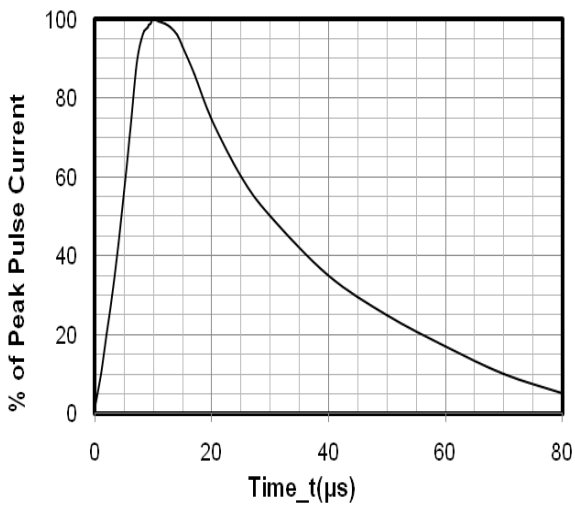
Peak Pulse Power vs. Pulse Time



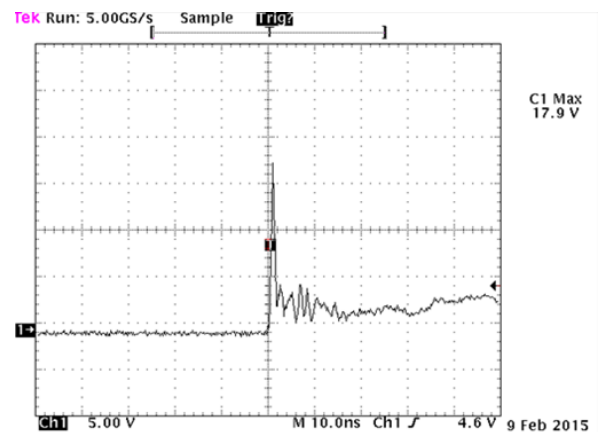
Clamping Voltage vs. Peak Pulse Current



Power Derating Curve



8 X 20μs Pulse Waveform



Note: Data is taken with a 10x attenuator

ESD Clamping Voltage

+8 kV Contact per IEC61000-4-2

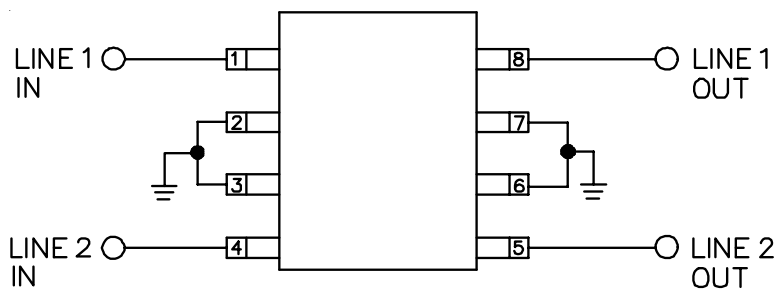
Applications Information

Device Connection Options for Protection of Two High-Speed Data Lines

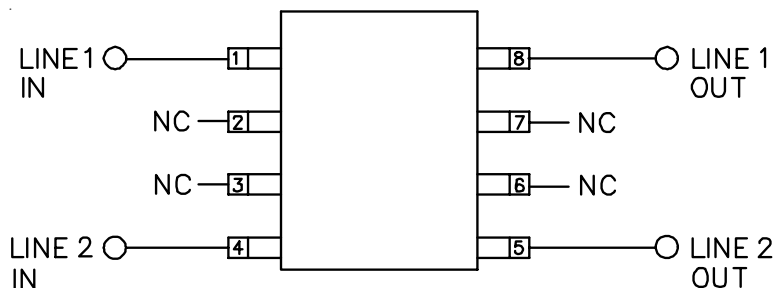
The LC03-3.3 is designed to protect two high-speed data lines (one differential pair) from transient over-voltages which result from lightning and ESD. The device can be configured to protect in differential (Line-to-line) and common (Line-to-Ground) mode. Data line inputs/outputs are connected at pins 1 to 8, and 4 to 5 as shown. Pins 2, 3, 6 and 7 are connected to ground. These pins should be connected directly to a ground plane on the board for best results. The path length is kept as short as possible to minimize parasitic inductance. In application where high common mode voltage are present, differential protection is achieved by leaving pins 2, 3, 6 and 7 not connected.

Matte Tin Lead Finish

Matte tin has become the industry standard lead-free replacement for SnPb lead finishes. A matte tin finish is composed of 100% tin solder with large grains. Since the solder volume on the leads is small compared to the solder paste volume that is placed on the land pattern of the PCB, the reflow profile will be determined by the requirements of the solder paste. Therefore, these devices are compatible with both lead-free and SnPb assembly techniques. In addition, unlike other lead-free compositions, matte tin does not have any added alloys that can cause degradation of the solder joint.

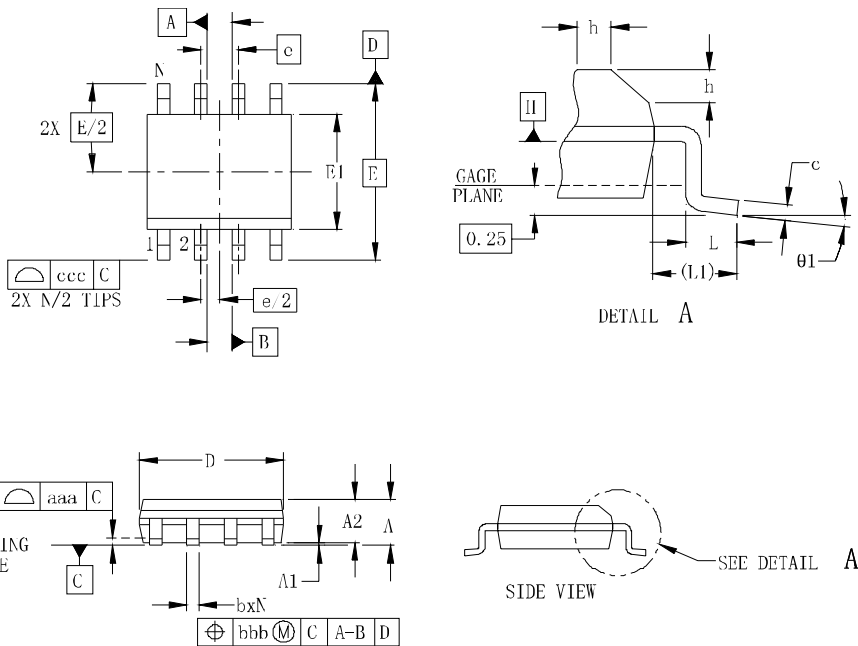


Connection for differential (Line to Line) and common mode protection (Line to Ground)



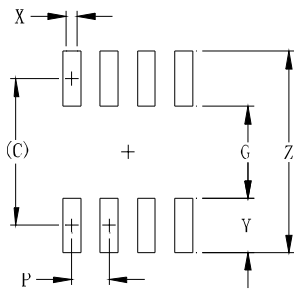
Connection for differential protection (Line to Line)

SO-8 Package Outline Drawing



SYM	DIMENSIONS					
	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	1.35		1.75	0.053		0.069
A1	0.10		0.25	0.004		0.010
A2	1.25		1.65	0.049		0.065
b	0.31		0.51	0.012		0.020
c	0.17		0.25	0.007		0.010
D	4.80	4.90	5.00	0.189	0.193	0.197
E1	3.80	3.90	4.00	0.150	0.154	0.157
E	6.00 BSC			0.236 BSC		
e	1.27 BSC			0.050 BSC		
h	0.25		0.50	0.010		0.020
L	0.40	0.72	1.04	0.016	0.028	0.041
L1	(1.04)			(0.041)		
N	8			8		
θ1	0°		8°	0°		8°
aaa	0.10			0.004		
bbb	0.25			0.010		
ccc	0.20			0.008		

Suggested Land Pattern

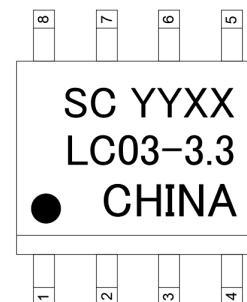


SYM	DIMENSIONS	
	MILLIMETERS	INCHES
C	(5.20)	0.205
G	3.00	0.118
P	1.27	0.050
X	0.60	0.024
Y	2.20	0.087
Z	7.40	0.291

Contact Information

Changzhou D-first Electronics CO.,Ltd.
 www.first-electronic.com
 Email: xhf@first-electronic.cn
 Phone: +86 (0519) 8817 1671

Marking Information



YYWW=Date Code
 Dot denotes Pin1